

Abstract of the Disclosure

5 A circuit arrangement that stacks a modulator and automatic gain control (AGC) amplifier so as to re-use the modulator output current to drive the AGC amplifier, thereby saving power consumption and avoiding an unnecessary signal transformation between the two stages. A balanced circuit topology is used. The modulation signal can be selected over a wide range (100 MHz to 5.4 GHz) and the circuit arrangement is therefore particularly suitable for wireless applications (e.g. GSM-EDGE) and for integrated circuit fabrication.